



3rd International Workshop on Software Product Line Testing

August 22, 2006, Baltimore, MD, USA

Call for Papers – *Deadline extended*

Product Line Engineering (PLE) has become a major topic in industrial software development and many organizations have started to consider PLE as state-of-practice. One topic that needs greater emphasis is testing of product lines. Product line testing is crucial to the successful establishment of PLE technology in an organization. The workshop aims at addressing some of the open fundamental challenges of testing in a PLE setting. Given the improvements in productivity that PLE delivers to development, how does a test organization keep pace? To what extent can we test reusable assets and how much can this reduce the testing obligations for each product? What properties of a product line architecture improve the testability of reusable assets and products and how can these be enforced during architectural design? Are there PLE techniques that can provide similar efficiency gains for testing as are possible for development? Without adequate answers, testing becomes the bottleneck in PLE.

Workshop Chairs:

Peter Knauber
Mannheim University of
Applied Sciences,
Germany
Charles Krueger
BigLever Software, USA
Tim Trew
Philips Research, The
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Program Committee:

For PLE:

Guenter Boeckle
Siemens AG, Germany
Krzysztof Czarnecki
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Georg Grütter, Robert
Bosch, Germany
Frank Roessler
Avaya Labs, USA

For Testing:

Stefan Jungmayr,
Teradyne Diagnostic
Solutions, Germany
Ronny Kolb
Fraunhofer IESE, Germany
John McGregor
Clemson University, USA

Important Dates:

Extended submission
deadline:

19 June 2006

Notification of acceptance:
10 July 2006

Camera ready version:
24 July 2006

We aim at bringing together both researchers and practitioners from testing and product line engineering on all aspects of product line testing, from designing for testability, through test coverage, to testing tools. We are especially interested in exchanging industrial experience in product line testing and comparing different approaches to enable an integration of different ideas. In this year's workshop the focus will be on strategies to evolve product line architectures towards more testability and to align the test strategy with the architecture.

Topics of interest include, but are not limited to:

- Testability patterns (architecture/design patterns & idioms for assuring testability) & anti-patterns
- Strategies to evolve product line architectures towards more testability and to align the test strategy with the architecture
- Test case design and test case generation for product lines
- Metrics and guidelines for component testability, test coverage, and test effectiveness in the context of software product lines
- Test-driven design in a product line context
- Minimizing redundant testing across product line members
- Test automation and testing tools
- Traceability issues from requirements to test cases
- Cost/benefit of core asset unit testing versus product integration testing

Paper Submission:

Authors should send position papers by email to:

split@biglever.com

Position papers should be no longer than 6 pages (conference format) and submitted as a PDF file. Submissions will be evaluated according to the relevance and originality of the work and to their ability to generate discussion among the workshop participants. The program committee is composed of experts from product-line engineering and testing. At least one testing and one product line expert will review each submission.

For more information please visit the workshop homepage at:
<http://www.biglever.com/split2006/>